



REPLACEMENT SHEET

Title: FUNCTIONAL TESTING OF LOGIC CIRCUITS
THAT USE HIGH-SPEED LINKS

Filing Date: 06/24/2003

Serial No.: 10/603,292

Atty. Docket No.: 042390P15839

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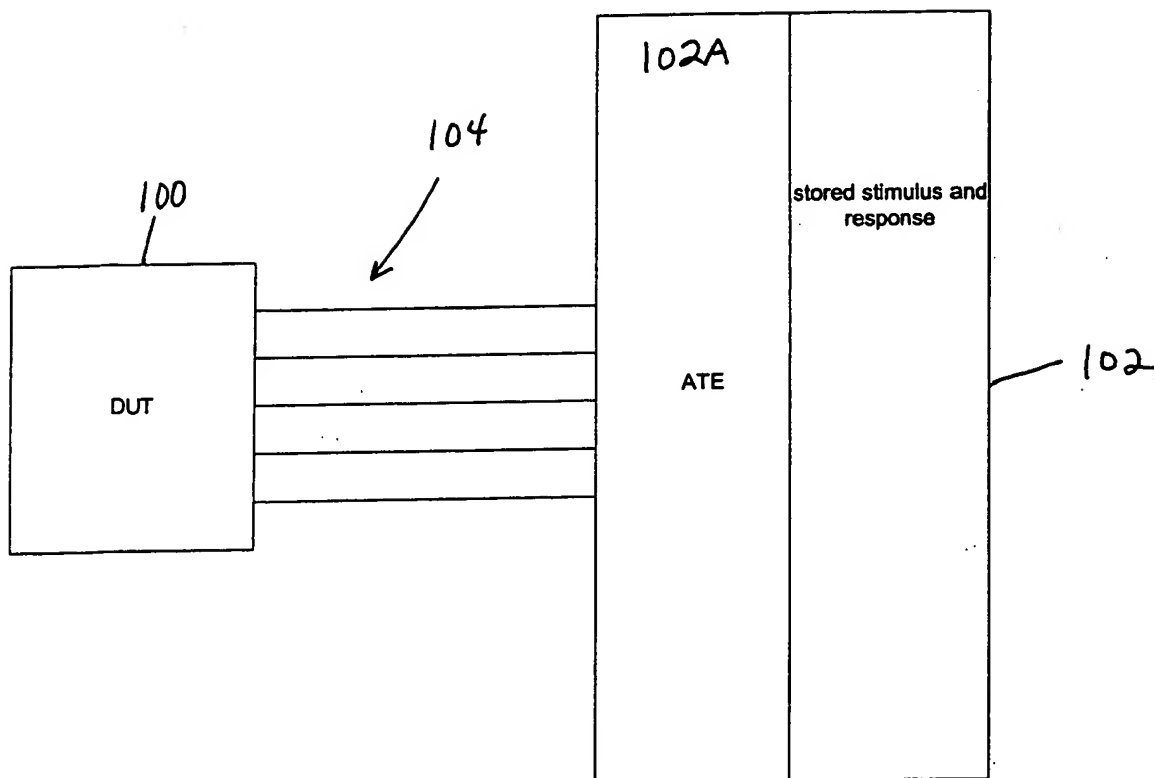
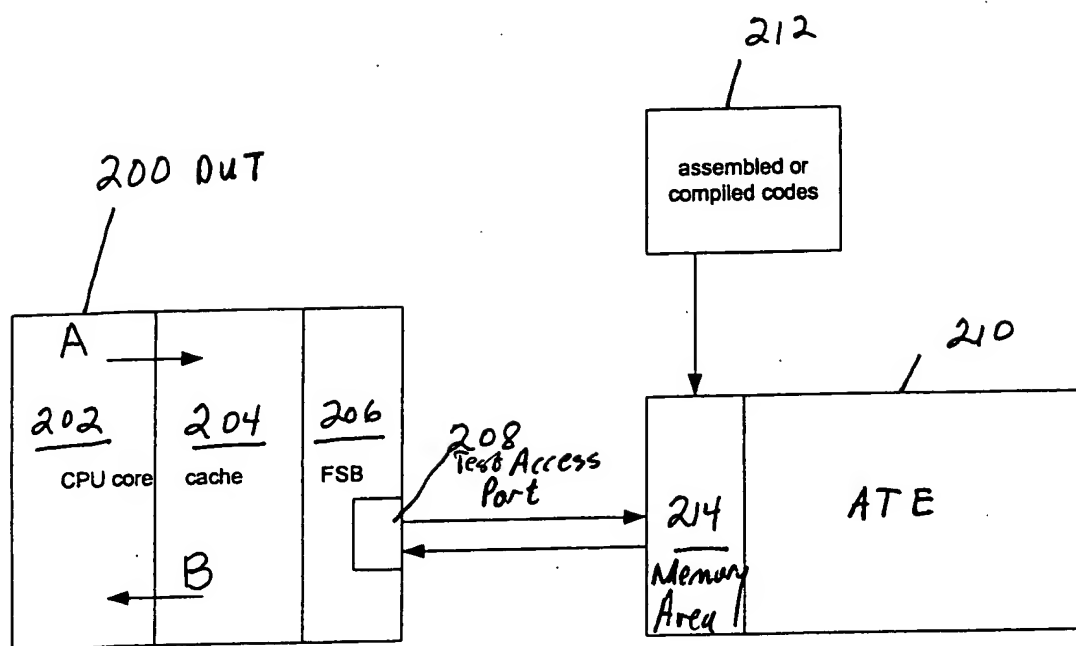


FIGURE 1
(Prior Art)

FIGURE 2
(Prior Art)